23000 Series PXI Backplanes

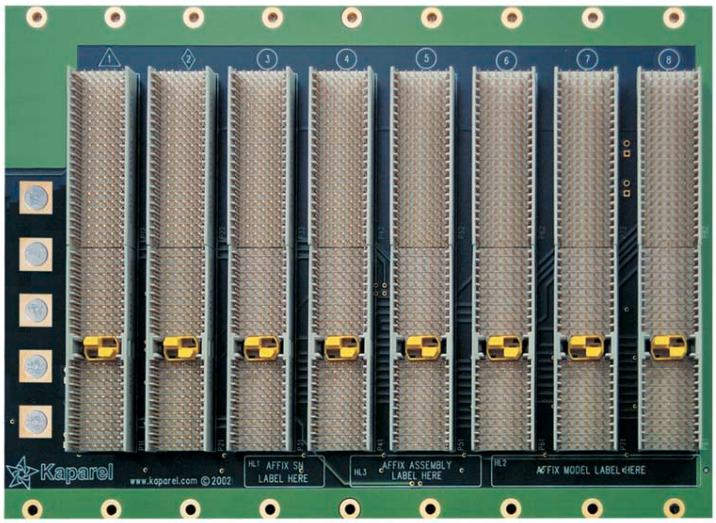
23000 Series 3U PXI Backplane:

General Description:

The Kaparel/Rittal 23000 Series of 3U PXI backplanes are PXI R2.0 and PICMG 2.8 draft specification compliant. These backplanes have one 64-bit CompactPCI bus and 3 to 7 peripheral slots. Kaparel/Rittal can also provide custom PXI backplane designs and configurations to suit particular customer applications.

Announced in August 1997, PXI is an open specification defining how PC technology can be successfully applied to measurement and automation. By leveraging CompactPCI, Microsoft Windows, and VXI, the PXI specification brings together the right technologies for PC-based text and measurement, instrumentation and industrial automation.

Model No.	PXI backplanes
PS23180	1 host slot, 17 peripheral slots
PS23400	1 host slot, 3 peripheral slots
PS23500	1 host slot, 4 peripheral slots
PS23600	1 host slot, 5 peripheral slots
PS23700	1 host slot, 6 peripheral slots
PS23800	1 host slot, 7 peripheral slots



23000 Series Module Allocation — Front Side Modules:

23180

01	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
Blank	System (SBC)	Star Trigger	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral

23400

01	1	2	3	4
Blank	System (SBC)	Star Trigger	Peripheral	Peripheral

23500

01	1	2	3	4	5
Blank	System (SBC)	Star Trigger	Peripheral	Peripheral	Peripheral

23600

01	1	2	3	4	5	6
Blank	System (SBC)	Star Trigger	Peripheral	Peripheral	Peripheral	Peripheral

23700

01	1	2	3	4	5	6	7
Blank	System (SBC)	Star Trigger	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral

23800

01	1	2	3	4	5	6	7	8
Blank	System (SBC)	Star Trigger	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral	Peripheral

Series Overview

23180 – 1 Host Slot, 17 peripheral slots
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23500 – 1 Host Slot, 4 peripheral slots
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Features:

- 4 to 8 slots wide backplanes routing one CompactPCI bus with one left justified SBC slot and one PXI bus
- 3U height (5.06 inches) supporting P1 and P2 connector positions
- Support for 2 to 7 3Ux4HP peripheral cards
- Support for one 3Ux4HP SBC card in physical slot 1
- Power entry via Power Bugs
- Complies with PICMG 2.1 R1.0 Hot Swap Specification
- Supports 32/64 bit, 33/66 MHz (66 MHz only on 5 or less slots)
- +3.3V or +5.0V V(I/O)
- Matched clock lines and ground guarding
- High current connector power entry
- User selectable clock source on or off-board
- PICMG 2.9 R1.0 compliant System Management Bus (IPMB)